10/650,465

PTO/SB/21 (08-03)
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Application Number

TRANS	Mi	T	AL
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(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Filing Date	August 27, 2003
First Named Inventor	Brian D. JOHNSON
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	306812002201

(Carolyn G. McKubre)

ENCLOSURES (Check all that apply)			
Fee Trans	mittal Form	Drawing(s)	After Allowance Communication to Group
Fee	Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences
Amendmer	nt/Reply	Petition	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
After	r Final	Petition to Convert to a Provisional Application	Proprietary Information
Affid	davits/declaration(s)	Power of Attorney, Revocation Change of Correspondence Address	Status Letter
Extension	of Time Request	Terminal Disclaimer	X Other Enclosure(s) (please identify below):
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x Information pages)	n Disclosure Statement (3	CD, Number of CD(s)	
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Response	to Missing Parts/ e Application	Remarks	
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Date	November 20, 200)3	
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PATENT Docket No.: 306812002201 Client Ref: A697 Continuation

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Complet My Solows

Carolyn McKubre

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Brian D. JOHNSON et al.

Serial No.: 10/650,465

Filing Date: August 27, 2003

For: USE OF DANGLING PARTIAL LINES

FOR INTERFACING IN A PLD

Examiner: Not Yet Assigned

Group Art Unit: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97 & 1.98

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, Applicants submit for consideration in the above-identified application the documents listed on the attached Form PTO-1449. Copies of documents numbered 1-34, 36-38, and 40-48 were previously submitted in an Information Disclosure Statement and/or Office Action, directed to the related application Serial Number 10/140,911, filed May 6, 2002, and, accordingly, copies are not included herewith. Pursuant to the USPTO notice dated July 11, 2003, waiving the requirement under 37 C.F.R.

§1.98 (a)(2)(i) to provide copies of U.S. Patents and U.S. Published Applications, copies of references 35 and 39 are not submitted. This protocol conforms with 37 C.F.R. §1.98(d) and M.P.E.P. 609(A)(2). The Examiner is requested to make these documents of record in the application.

	i nis in	formation Disclosure Statement is submitted:
	With	the application; accordingly, no fee or separate requirements are required.
	Befor	te the mailing of a first Office Action after the filing of a Request for Continued
	Exam	ination under § 1.114.
\boxtimes	Withi	n three months of the application filing date or before mailing of a first Office
	Actio	n on the merits; accordingly, no fee or separate requirements are required.
	After	receipt of a first Office Action on the merits but before mailing of a final Office
	Actio	n or Notice of Allowance.
		A fee is required. A check in the amount of is enclosed.
		A fee is required. Accordingly, a Fee Transmittal form (PTO/SB/17) is attached
		to this submission in duplicate.
		A Certification under 37 C.F.R. § 1.97(e) is provided below; accordingly; no fee
		is believed to be due.
	After	mailing of a final Office Action or Notice of Allowance, but before payment of the
	issue	fee.
		A Certification under 37 C.F.R. § 1.97(e) is provided below and a check in the
		amount of is enclosed.
		A Certification under 37 C.F.R. § 1.97(e) is provided below and a Fee Transmittal
		form (PTO/SB/17 is attached to this submission in duplicate.)

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

The information contained in this Information Disclosure Statement under 37 C.F.R. § 1.97 and § 1.98 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

In the unlikely event that the transmittal form is separated from this document and the Patent Office determines that an extension and/or other relief is required, Applicants petition for any required relief including extensions of time and authorize the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing 306812002201 However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: November 2003

Respectfully submitted,

By:

Alan S. Hodes Registration No. 38,185

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pa-834747

Serial No. 10/650,465 Docket No. 306812002201 01PE 102 24 2003 22

Form PTO-1449

INFORMATION DECEMBER CITATION IN AN APPLICATION

(Use several sheets if necessary)

	Sheet 1 01 3
Docket Number 306812002201	Application Number 10/650,465
Applicant	
Brian D.	. JOHNSON et al.
Filing Date August 27, 2003	Group Art Unit Not Yet Assigned
Mailing Date November 20, 200	03

U.S. PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate
	1.	10/25/2001	2001/0033188	Aung et al.			
•	2.	09/26/1989	4,870,302	Freeman			
	3.	10/03/1989	4,871,930	Wong et al.			
•	4.	06/09/1992	5,121,006	Pedersen			
	5.	08/31/1993	5,241,224	Pedersen et al.			·
	6.	09/07/1993	5,243,238	Kean			
	7.	11/09/1993	5,260,611	Cliff, et al.			
	8.	10/25/1994	5,359,536	Agrawal et al.			
	9.	10/03/1995	5,455,525	Ho, et al.			
	10.	01/16/1996	5,485,103	Pedersen, et al.			
	11.	07/16/1996	5,537,057	Leong, et al.			
	12.	07/30/1996	5,541,530	Cliff, et al.			
	13.	08/27/1996	5,550,782	Cliff et al.			
	14.	09/17/1996	5,557,217	Pedersen			
	15.	12/03/1996	5,581,199	Pierce, et al.			
	16.	01/07/1997	5,592,106	Leong, et al.			
	17.	10/28/1997	5,682,107	Tavana, et al.			
	18.	11/18/1997	5,689,195	Cliff, et al.			
	19.	12/23/1997	5,701,091	Kean			
	20.	01/06/1998	5,705,939	McClintock, et al.			
	21.	06/02/1998	5,760,604	Pierce et al.			
	22.	12/08/1998	5,847,579	Trimberger			
	23.	03/09/1999	5,880,598	Duong			
	24.	05/11/1999	5,903,165	Jones et al.			
	25.	05/25/1999	5,907,248	Bauer, et al.			
	26.	06/01/1999	5,909,126	Cliff, et al.			

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

7 4 2003 Sheet 2 of 3 Docket Number 306812002201 Application Number 10/650,465 Form PTO-1449 INFORMATION DISCLOSUS CITATION Applicant Brian D. JOHNSON et al. IN AN APPLICATION Filing Date August 27, 2003 (Use several sheets if necessary) Group Art Unit Not Yet Assigned . Mailing Date November 20, 2003 27. 06/22/1999 5,914,616 Young, et al. 28. 08/24/1999 5,942,913 Young, et al. 29. 11/02/1999 5,977,793 Reddy et al. Sasaki et al. 30. 1214/1999 6,002,268 31. 07/04/2000 6,084,429 Trimberger 32. 08/22/2000 6,107,824 Reddy, et al. 03/20/2001 33. 6,204,690 Young, et al. 34. 04/17/2001 6,218,859 Pedersen 35. 08/21/2001 6,278,291 McClintock, et al. 36. 09/11/2001 6,289,412 Yuan et al. 37. 09/18/2001 6,292,018 Kean 38. 10/09/2001 6,300,794 Reddy, et al. 39. 11/19/2002 6,484,291 Amiya et al. 40. 08/12/2003 6,605,962 Lee et al. FOREIGN PATENT DOCUMENTS Examiner Ref. Document No. Subclass Date Country Class Translation **Initials** No. YES NO OTHER DOCUMENTS (including author, title, Date, Pertinent Pages, Etc.) Examiner Ref. Title Initials No. 41. Altera Corporation (June 1996). "Max 7000: Programmable logic device family" Data Book, A-DB-0696-01, version 4, pp. 193-261. Altera Corporation (October 2001). Excalibur "Description of the The Floor plan", pp. 1-1 through 42. 1-10. Betz, V. et al., eds. (1999) "Background and Previous Work," Chapter 2 In Architecture and CAD 43. for Deep-Submicron FPGAs, Kluwer Academic, Boston pp. 12-18. 44. Betz, V. et al., eds. (1999). "Routing Tools and Routing Architecture Generation," Chapter 4 In Architecture and CAD for Deep-Submicron FPGAs, Kluwer Academic, Boston, pp. 63-95. 45. Betz, V. et al., eds. (1999). "Detailed Routing Architecture," Chapter 7 In Architecture and CAD for Deep-Submicron FPGAs, Kluwer Academic, Boston, pp.151-190. DATE CONSIDERED: **EXAMINER:** EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.



Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket	Number	20691	2002201
DOCKEL	Number	SOODI	2002201

Application Number 10/650,465

Applicant

Brian D. JOHNSON et al.

Filing Date August 27, 2003

Group Art Unit Not Yet Assigned

Mailing Date November 20, 2003

46.	Betz, V. et al., eds. (1999). "Global Routing Architecture," Chapter 5 In <u>Architecture and CAD for Deep-Submicron FPGAs</u> , Kluwer Academic, Boston, pp. 105-126.
47.	Xilinx. (November 2000). "Programmable Logic: News and Views, a montly report on developments in the PLD/FPGA industry" <i>Electronic Trend Publications</i> , <i>Inc.</i> , IX(11):14-15. (Includes Table of Contents).
48.	Xilinx. (October 2000) "Putting It All the Together. The Vitrex-II Series Platform FPGA: World fastest logic and routing" Xilinx brochure, p. 83.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.